

Docket Number  
36856.1458

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Osamu CHIKAGAWA	Art Unit: 2841
Application No.: 10/597,919	
Confirmation No.: 7894	Examiner: J. Norris
Filing or 371(c) Date: August 11, 2006	
Title: MULTILAYER SUBSTRATE WITH BUILT-IN CHIP-TYPE ELECTRONIC COMPONENT AND METHOD FOR MANUFACTURING THE SAME	

**AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 24, 2009, the period for response to which has been extended to October 26, 2009 (October 24, 2009 falls on a Saturday), by the accompanying Petition for One-Month Extension of Time, please amend the above-identified application as follows:

- Amendments to the Specification** begin on page \_\_\_\_\_ of this paper.
- Amendments to the Claims** are reflected in the listing of the claims which begins on page 2 of this paper.
- Amendments to the Drawings** begin on page \_\_\_\_\_ of this paper and include an attached replacement sheet. An **Appendix** including the amended drawing figures is attached following page \_\_\_\_\_ of this paper.
- Remarks/Arguments** begin on page 4 of this paper.

Please note, if a box is not checked, then no corresponding amendment is being made.